Step-up PWM DC/DC Converter

GENERAL DESCRIPTION

The AX5204 is high efficient step-up DC/DC converter. Large output current is possible having a built in internal N channel MOSFET, and using an external coil and diode.

The AX5204 can be operated at switching frequencies of 500 kHz allowing for easy filtering and low noise, the size of the external components can be reduced.

Output voltage is programmable with 1.0V of standard voltage supply internal, and using externally connected components, output voltage (FB) can be set up at will. The soft-start time can be programmed by outside capacitor; the function prevents overshoot at startup. Build inside Current limit, Thermal Shutdown and enable functions.

❖ FEATURES

Input voltage: 3V to 20V

Output voltage: 3.3V to 32V

Duty ratio: 0% to 85% PWM control

Oscillation frequency: 500KHz.

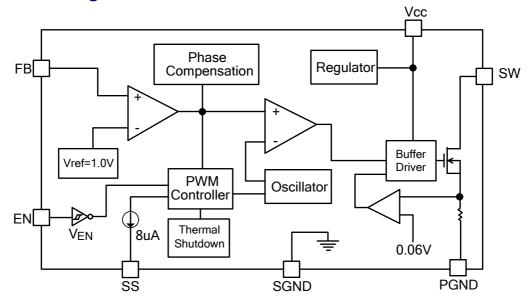
Enable and Thermal Shutdown function.

Internal Current limit.

Built-in N-channel MOSFET

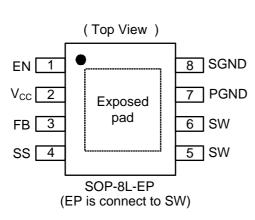
SOP-8L with Exposed pad Pb-Free package.

Block Diagram



PIN ASSIGNMET

The package of AX5204 is SOP-8L-EP; the pin assignment is given by:



| Name | Description | | | | |
|----------|-------------------------------------------------------------------------|--|--|--|--|
| SGND | Signal Ground pin. | | | | |
| PGND | Power Ground pin | | | | |
| EN | Power-off pin H: normal operation(Step-up) L: Step-up operation stopped | | | | |
| V_{CC} | IC power supply pin | | | | |
| FB | Feedback pin | | | | |
| SW | Switch pin. Connect external inductor & diode here. | | | | |
| SS | Soft-Start Pin. | | | | |

❖ ORDER/MARKING INFORMATION

| Order Information | Top Marking | | |
|-------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|
| AX5204 X X Package Type Packing ES: SOP-8L Blank: Tube A: Taping | Logo \leftarrow \bullet \bullet \bullet \bullet Part number $ \underbrace{X \ X \ X \ X}_{\bullet} \bullet \bullet$ ID code: internal $ \bullet$ WW: 01~52 $ \bullet$ Year: 07 = 2007 | | |

❖ Absolute Maximum Ratings (at Ta=25°C)

| Characteristics | Symbol | Rating | Unit |
|---------------------------------------------|----------------------|------------------------------|------------------------|
| VCC Pin Voltage | Vcc | GND - 0.3 to GND + 22 | V |
| Feedback Pin Voltage | V_{FB} | GND - 0.3 to 6 | V |
| ON/OFF Pin Voltage | V_{EN} | GND - 0.3 to V _{CC} | V |
| Switch Pin Voltage | V _{SW} | GND - 0.3 to 34 | V |
| SS Pin Voltage | V_{SS} | GND - 0.3 to 6 | V |
| Power Dissipation | PD | Internally limited | mW |
| Storage Temperature Range | T _{ST} | -40 to +150 | $^{\circ}\!\mathbb{C}$ |
| Operating Junction Temperature | T _{OPJ} | -20 to +125 | $^{\circ}\!\mathbb{C}$ |
| Thermal Resistance from Junction to case | θ_{JC} | 15 | °C/W |
| Thermal Resistance from Junction to ambient | θја | 40 | °C/W |

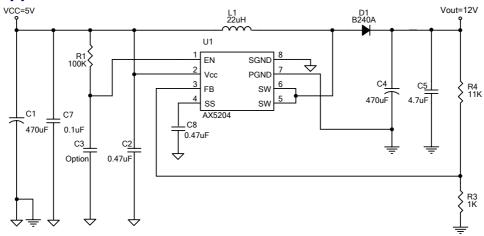
Note: θ JA is measured with the PCB copper area (connect to exposed pad) of approximately 1 in²(Multi-layer).



❖ Electrical Characteristics (Vcc = 5V, Vouт=12V, Ta=25°C, unless otherwise specified)

| Characteristics | Symbol | Conditions | | Min | Тур | Max | Units |
|-----------------------------------|---------------------|----------------------------------------------|-------------------------|------|------|------|------------------------|
| Operating Supply Voltage | V_{CC} | | | 3 | - | 20 | V |
| Output Voltage Range | V _{OUT} | | | 3.3 | - | 28 | V |
| Feedback Voltage | V_{FB} | I _{OUT} =0.1A | | 0.98 | 1.00 | 1.02 | V |
| Feedback Bias Current | I _{FB} | I _{OUT} =0.1A | | - | 0.1 | 0.5 | uA |
| Quiescent Current | I _{CCQ} | V _{FB} =1.5V forc | e driver off | • | 4 | 6 | mA |
| Shutdown Supply Current | I_{SD} | V _{EN} =0V | | 1 | 1 | 10 | uA |
| Oscillation Frequency | Fosc | SW pin | | 400 | 500 | 600 | KHz |
| Line Regulation | | V _{CC} =3~0.8*V | out | - | 1 | - | % |
| Load Regulation | | I _{OUT} =50m~0.6 | iΑ | - | 0.5 | - | % |
| EN Pin Logic input | V_{SH} | High (regulate | or ON) | 2.0 | - | - | V |
| threshold voltage | V _{SL} | Low (regulator OFF) | | - | - | 0.8 | \ \ \ \ \ |
| EN Din Innut Current | I _{SH} | V _{EN} =2.5V (ON) | | - | 20 | - | uA |
| EN Pin Input Current | I _{SL} | V _{EN} =0.3V (OFF) | | - | -1 | - | uA |
| SS pin Current | I _{SS} | | | - | 8 | - | uA |
| Switching Current Limit | I _{LIM-SW} | | | 1.8 | 2.0 | - | Α |
| Internal MOCEET D | R _{DSON} | V _{CC} =5V | | - | 40 | 80 | 0 |
| Internal MOSFET R _{DSON} | | V _{CC} =12V | | - | 30 | 60 | mΩ |
| Efficiency | EFFI | V _{CC} =5V V _{OUT} =12V | I _{OUT} = 0.8A | - | 91 | - | % |
| Maximum Duty Cycle | DC _{MAX} | V _{FB} =0V | | - | 85 | - | 0/ |
| Minimum Duty Cycle | DC _{MIN} | V _{FB} =1.5V | | - | 0 | - | - % |
| Thermal shutdown Temp | TSD | | | - | 145 | - | $^{\circ}\!\mathbb{C}$ |

Application Circuit



$$V_{OUT} = V_{FB} \times (1 + \frac{R4}{R3}), V_{FB} = 1.0V, R3 = 1K \sim 3K$$

Function Descriptions

PWM Control

The AX5204 consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the AX5204, the pulse width varies in a range from 0 to 85%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

Setting the Output Voltage

Application circuit item shows the basic application circuit with AX5204 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{out} = 1.0V \times \left(1 + \frac{R4}{R3}\right)$$

Table 1 Resistor select for output voltage setting

| V _{OUT} | R3 | R4 |
|------------------|------|-----|
| 12V | 1K | 11K |
| 15V | 1.3K | 18K |
| 18V | 1.3K | 22K |
| 24V | 1.3K | 30K |
| 32V | 2.2K | 68K |

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Inductor Selection

For most designs, Low inductance values are physically smaller but require faster switching, which results in some efficiency loss. The inductor value can be derived from the following equation:

$$L = \frac{V_{IN} \times (V_{OUT} - V_{IN})}{V_{OUT} \times \Delta I_{L} \times f_{IX}}$$

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 15% of the maximum input current 1.6A, ΔI_L =0.24A.

Table 2 Inductor select for output voltage setting (V_{CC}=5V)

| | | | | · |
|------------------|------|------|------|------|
| V _{out} | 9V | 12V | 15V | 18V |
| L1 Value | 18uH | 22uH | 25uH | 33uH |

The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (1.6A+0.12A).

Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used.

The capacitor voltage rating should be at least 1.5 times greater than the input voltage, and often much higher voltage ratings are needed to satisfy.

Output Capacitor Selection

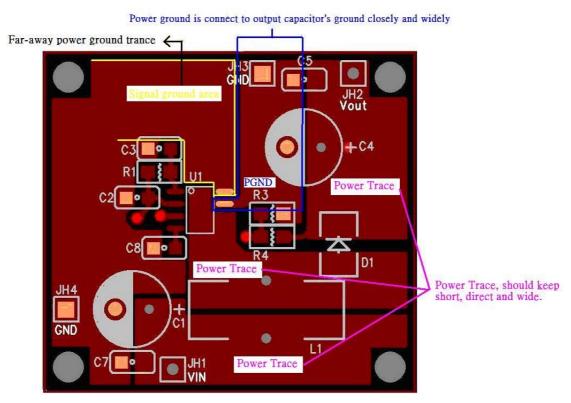
The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. A low ESR capacitor sized for maximum RMS current must be used. The low ESR requirements needed for low output ripple voltage.

The capacitor voltage rating should be at least 1.5 times greater than the input voltage, and often much higher voltage ratings are needed to satisfy.

Layout Guidance (please refer layout picture)

When laying out the PC board, the following suggestions should be taken to ensure proper operation of the AX5204. These items are also illustrated graphically in below.

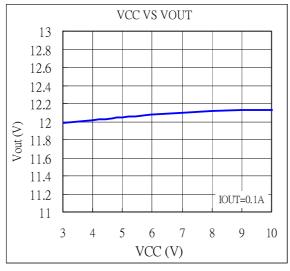
- 1. The power traces, including the Source trace, the Schottky and the C1 trace should be kept short, direct and wide to allow large current flow.
- 2. The power ground is keep C4's ground closed and far away signal ground.
- 3. The signal ground trance is distant from power ground trance.
- 4. The exposed pad is connecting to SW trace closely and widely. (Reduce IC temperature)
- 5. Do not trace signal line under inductor.

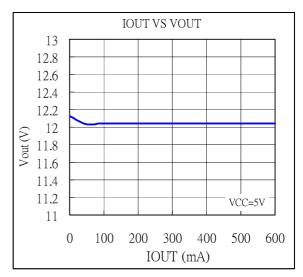


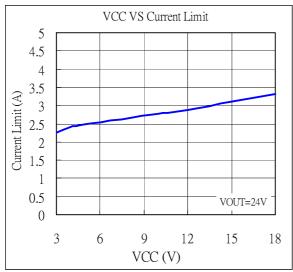
(AX5204 PCB Layout -Top View)

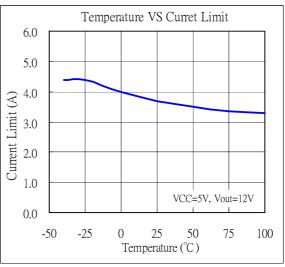


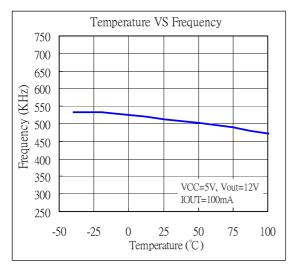
❖ Typical Characteristics

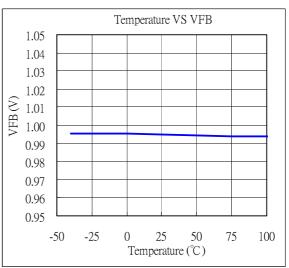




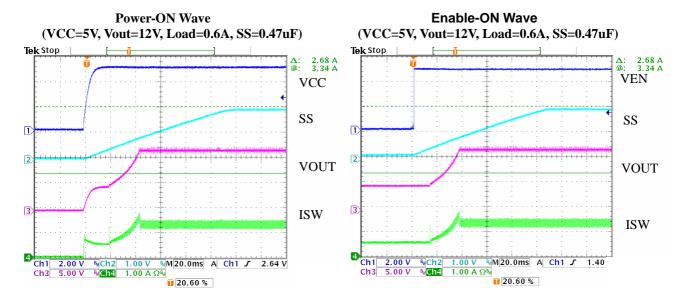






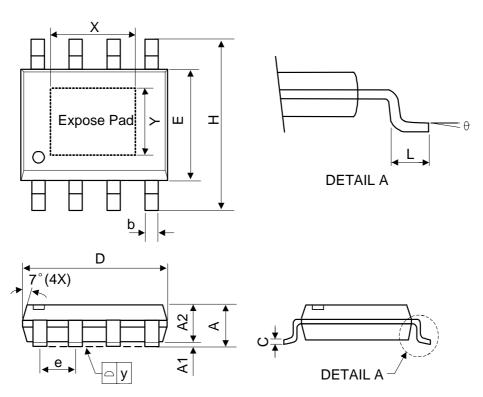


Typical Characteristics (Continuous)





❖ Package Outlines



| Symbol | Dimens | Dimensions In Millimeters | | Dimensions In Inches | | | |
|----------|------------|---------------------------|------------|----------------------|-----------|-------|--|
| Syllibol | Min. | Nom. | Max. | Min. | Nom. | Max. | |
| Α | 1.40 | 1.60 | 1.75 | 0.055 | 0.063 | 0.069 | |
| A1 | 0.10 | - | 0.25 | 0.040 | - | 0.100 | |
| A2 | 1.30 | 1.45 | 1.50 | 0.051 | 0.057 | 0.059 | |
| С | 0.19 | 0.20 | 0.25 | 0.0075 | 0.008 | 0.010 | |
| D | 4.80 | 4.90 | 5.00 | 0.189 | 0.193 | 0.197 | |
| Е | 3.80 | 3.90 | 4.00 | 0.150 | 0.154 | 0.157 | |
| Н | 5.79 | 5.99 | 6.20 | 0.228 | 0.236 | 0.244 | |
| L | 0.38 | 0.71 | 1.27 | 0.015 | 0.028 | 0.050 | |
| b | 0.33 | 0.41 | 0.51 | 0.013 | 0.016 | 0.020 | |
| е | e 1.27 TYP | | | | 0.050 TYP | | |
| у | - | - | 0.10 | - | - | 0.004 | |
| X | - | 2.34 | - | - | 0.092 | - | |
| Υ | | 2.34 | - | - | 0.092 | - | |
| θ | 0 0 | - | 8 0 | 00 | - | 80 | |