# TG1B1-A Datasheet

# 10G BERT





## 特性：

内部及外部的时钟系统

0.5-12Gbps工作范围

多种时钟和形式触发

多个或单个的输入和输出端子

可编程的输出多种控制

可选的伪随机二进制序列模式长度，脉冲密度

本地按钮操作和远程的通用接口总线

## Clock System Specifications

| | Units | Min | Typ | Max |
|---|---|---|---|---|
| (TXCKO,RXCKO) High Frequency Clock | GHz | 9.85 | - | 11.35 |
| Output Amplitude | Vpp | 0 | - | 2 |
| Resolution GPIB | MHz | - | 1 | - |
| Resolution Front Panel | MHz | - | 10 | - |
| Jitter RMS | Ps | - | 0.9 | 1.5 |
| External Interface | AC Coupled Female SMA 50 ohms | | | |
| HF TrigO Frequency Range | GHz | 9.85 | - | 11.35 |
| HF TrigO Amplitude | Vpp | - | 1.3 | - |
| External Interface | AC Coupled Female SMA 50 ohms | | | |
| LF TrigO Frequency Range | MHz | 615.625 | - | 709.375 |
| LF TrigO Amplitude | Vpp | - | 0.7 | - |
| External Interface | AC Coupled Female SMA 50 ohms | | | |
| Jitter Input Frequency Range | MHZ | DC | - | 100 |
| Jitter Amplitude | Max 15UI < 100kHz, 0.4UI > 10 MHz | | | |
| Jitter Outputs | Out, NotOut, TX CKO, HF TrigO jittered, (RX CKO unjittered) | | | |
| Interface | DC Coupled Female SMA 50 ohms | | | |